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J&R-0694

CERTIFICATION OF MAILING OR TRANSMISSION

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SignatureAugust 20, 2007

DateIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/922,479 Confirmation No. 7212
Applicant : Thomas Zettler
Filed : August 3, 2001
Title : Method and Device for Testing an Integrated
Circuit, Integrated Circuit to be Tested, and
Wafer with a Large Number of Integrated
Circuits to be Tested
Group Art Unit: 2117
Examiner : Phung My Chung
Docket No. : J&R-0694
Customer No. : 24131

R E S P O N S E**Mail Stop Amendment**

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office Action dated May 21, 2007, kindly
consider the following:

Remarks/Arguments begin on page 2 of this paper.